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IDENTIFICATION

PRODUCT CODE: AC-E9238-MC
PRODUCT NAME: CXADC80 ADV11 MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0 ABSTRACT

ADC IS AN IOMOD THAT EXERCISES THE ADV11 ANALOG MODULE. THIS MODULE REQUIRES ONLY AN ANALOG GROUND ON CHANNEL ZERO IN ORDER TO BE RUN. HOWEVER, WITH SPECIAL SETUP, MORE OPTIONS CAN BE CHOSEN. ONE OPTION IS THE USE OF THE KW11 (REAL TIME CLOCK) WITH THE THIS OPTION ALLOWS THE EXERCISING OF THE ADV11 ASYNCHRONOUSLY WITH THE CPU. THIS IS ADV11 CONVERSIONS WILL BE STARTED AT RANDOM TIMES TO ALLOW FOR MEMORY BUS NOISE DURING THE CONVERSION. IF THIS OPTION IS SELECTION, YOU MUST DESELECT KWE FROM A DEC/Y11 ON CHANNEL ZERO AND COMPARE AGAINST A LIMIT WITH THE SAMPLED OPERATION OPTION. MORE CHANNELS MAY BE SPECIFIED TO RUN THE SECOND TESTS ON. THE THIRD OPTION ALLOWS FOR SAMPLING OF ONE TO ALL THE CHANNELS OF THE ADV11. A CHECK IS MADE TO SEE THAT THE INPUT VOLTAGE REMAINS STABLE WITHIN AN ALLOWED TOLERANCE. LOCATIONS WITHIN THIS MODULE ARE PROVIDED TO CHANGE ANY LIMIT, OR TO FORCE ANY VALUE.

TYPEOUT OF

2.0 REQUIREMENTS

HARDWARE: ONE ADV11
ONE BERG CONNECTOR (OPTIONAL)
ONE KW11 (OPTIONAL)

STORAGE:: ADC REQUIRES:
1. DECIMAL WORDS: 933
2. OCTAL WORDS: 1645
3. OCTAL BYTES: 3512

3.0 PASS DEFINITION

ONE PASS OF THE ADC MODULE CONSISTS OF GENERATING 8244(DECIMAL) INTERRUPTS (CONVERSIONS).

4.0 EXECUTION TIME

ONE PASS OF THE ADC MODULE RUNNING ALONE TAKES APPROXIMATELY ONE MINUTE.

5.0 CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 170400, VECTOR: 400, BRI: 6
DEVcnt: 1, SRI: 0

REQUIRED PARAMETERS:

NONE IF SRI=1

IF SRI BITS 1 2=1 THEN SEE OPERATION OPTIONS

6.0 DEVICE/OPTION SETUP

SRI=000 AN ANALOG GROUND MUST BE PUT ON CH. 0.

SRI BIT0=1 THE KWV11 OPTION MUST BE CONNECTED TO THE ADV11 OPTION.

SRI BIT1=1 ALL CHANNELS SPECIFIED MUST HAVE AN ANALOG GROUND.

7.0 MODULE OPERATION

1. (START) BIT EXERCISE CSR

2. SET TEST CHANNEL TO ZERO

3. (RSTRT) PERFORM RMS NOISE CHECK ON SPECIFIED CHANNEL.

(AD RMS1)
WE FIRST USE SAR TO FIND THE DAC VALUE THAT PRODUCES A 16/84 SPLIT FOR THE LEFT BOUNDARY OF NOISE. THEN WE USE SAR TO FIND THE DAC VALUE THAT PRODUCES A 84/16 SPLIT FOR THE RIGHT VALUE. WE THEN SUBTRACT THE TWO DAC VALUES AND WE HAVE A VALUE FOR THE 68% AREA OF NOISE (RMS). THEN WE COMPARE AGAINST THE ALLOWED LIMIT TO SEE IF EXCESSIVE NOISE IS ON THE CHANNEL.

4. (ADPK1) PERFORM PEAK NOISE CHECK ON SPECIFIED CHANNEL.

(ADPK1) THIS IS A PEAK NOISE TEST.
WE FIRST USE SAR TO FIND THE DAC VALUE THAT PRODUCES A 6% SPLIT FOR THE LEFT BOUNDARY OF NOISE. THEN WE USE SAR TO FIND THE DAC VALUE THAT PRODUCES A 6% SPLIT FOR THE RIGHT BOUNDARY. WE THEN SUBTRACT THE TWO DAC VALUES AND WE HAVE A

VALUE OF 98% AREA OF NOISE (PEAK). IT IS THEN COMPARED AGAINST THE ALLOWED LIMIT TO SEE IF EXCESSIVE NOISE IS ON THE PAGE 4

CHANNEL.

5. IF MULTIPLE CHANNELS ARE SELECTED FOR NOISE TESTING TEST NEXT CHANNEL, IF SINGULAR REPEAT CHAN. 0.
6. IF MULTI-CHANNEL SAMPLING IS SELECTED, TAKE SAMPLES ON EACH CHANNEL SPECIFIED AND COMPARE THE AVERAGE OF THE SAMPLES AGAINST THE OLD AVERAGE FOR THE CHANNEL. IF THE DIFFERENCE IS GREATER THAN THE TOLERANCE, REPORT THE DATA ON THAT CHANNEL.
7. REPORT END PASS.
8. (SAR) SAR IS A SUCCESSIVE APPROXIMATION ROUTINE. IT IS USED TO FIND A DAC VALUE THAT PRODUCES A DESIRED SPLIT. IT DOES THIS BY TRYING A DAC VALUE AND TAKING 512 CONVERSIONS ON THE A/D. IF THE AMOUNT OF THE SAMPLES IS LOWER THEN SPECIFIED IT INCREASES THE DAC VALUE IF THE AMOUNT OF SAMPLES IS HIGHER THEN SPECIFIED, IT DECREASES THE DAC VALUE. IF THE END DAC VALUE IS EITHER 000 OR 377 WE HAVE A "WARP AROUND" ERROR. THIS OCCURS WHEN WE ARE UNABLE TO ADJUST THE DAC TO PRODUCE A DESIRED SPLIT, AND INDICATES EXCESSIVE NOISE ON A CHANNEL.
9. (RANDY) THIS IS A RANDOM NUMBER GENERATOR. IF THE KRV11 CLOCK OPTION IS SELECTED WE GET THE NUMBER THAT WE PUT INTO THE CLOCK PRESET REGISTER FROM THIS ROUTINE.

8.0 OPERATION OPTIONS

1. VALID SRI VALUES

SRI BIT	ENABLE/DISABLE	FUNCTION
0	0	INHIBIT USE OF CLOCK OPTION.
0	1	ENABLE USE OF CLOCK OPTION. NOTE: IF ENABLED, YOU MUST DESELECT KWEA FROM DEC/X11 R/W.
1	0	INHIBIT SAMPLING OTHER CHANNELS FOR STABLE INPUT.
1	1	ENABLE SAMPLING CHANNEL ZERO THROUGH CHANNEL SPECIFIED BY CLSTCH FOR STABLE INPUT (+) TOLERANCE SPECIFIED BY OFFALL
2	0	USE CHANNEL ZERO ONLY FOR NOISE TESTING.
2	1	USE CHANNEL ZERO THROUGH THE CHANNEL SPECIFIED IN NLSTCH FOR NOISE TESTING.

2. THE FOLLOWING ARE LOCATIONS WITHIN THIS MODULE THAT ENABLE THE USER TO CHANGE LIMITS AND SPECIFY CHANNELS.

LOCATION	FUNCTION
ARMLIM	SPECIFIES MAXIMUM LIMIT FOR RMS NOISE BE CHANGED TO ZERO TO FORCE TYPHOOT OR RMS NOISE ON A CHANNEL RUNNING IN A SYSTEM ENVIRONMENT.
APKLM	SPECIFIES MAXIMUM LIMIT FOR PEAK NOISE. MAY BE CHANGED TO ZERO TO FORCE TYPHOOT OF PEAK NOISE ON A CHANNEL RUNNING IN A SYSTEM ENVIRONMENT.
CLSTCH	IF SRI BIT1=1, USED TO SPECIFY END CHANNEL FOR SAMPLING STABLE INPUT.
OFFALL	IF SRI BIT1=1, USED TO SPECIFY TOLERANCE OF STABLE CHANNEL. PRESET BY MODULE TO "000002".
NLSTCH	166 IF SRI BIT2=1, USED TO SPECIFY END CHANNEL FOR NOISE TESTING.

9.0 NON-STANDARD PRINTOUTS

1. IF A CHANNEL HAS EXCESSIVE RMS NOISE, IT REPORTS IT IN AN ERROR CALL AND A MSGN CALL:
(EXAMPLE)
ON CH. 00 A/D RMS NOISE=0.52 LSB (LIMIT=.25LSB)
2. IF A CHANNEL HAS EXCESSIVE PEAK NOISE, IT REPORTS IT IN AN ERROR CALL AND A MSGN CALL:
(EXAMPLE)
ON CH. 00 A/D PEAK NOISE=2.57LSB(LIMIT=2.00LSB)
3. IF THERE IS AN EXCESSIVE AMOUNT OF NOISE SO THAT THE DAC CANNOT BE ADJUSTED, IT REPORTS IT IN AN ERROR CALL AND A MSGN CALL:
(EXAMPLE)
PEAK WRAPAROUND ERROR ON CHAN. 00
4. IF A CHANNEL IS FOUND TO BE UNSTABLE IN STABLE INPUT SAMPLING, IT REPORTS IT IN A MSGN CALL:
(EXAMPLE)
ON CHAN 14 OLD AVERAGE=4065 NEW AVERAGE=4000

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      ;
      ; TITLE ADCB DEC/X11 SYSTEM EXERCISER MODULE
      ; DDXCOM VERSION 6 23-MAY-78
      ; ***** LIST *****
      ; ***** LIST *****
000000- BEGIN:
000000- 042101 041103 040 ; *****
000005- 000 ; ASCII /ADCB / ;MODULE NAME
000006- 170400 XFLAG: -BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
000010- 000400 ADDR: 170400+0 ;1ST DEVICE ADDR.
000012- 300 VECTOR: 400+0 ;1ST DEVICE VECTOR.
000013- 200 BR1: -BYTE PRTV6+0 ;1ST RR LEVEL.
000014- 000001 BR2: -BYTE PRTV4+0 ;2ND RR LEVEL.
000016- 000000 DVID1: +1 ;DEVICE INDICATOR 1.
000020- 000000 SR1: OPEN ;SWITCH REGISTER 1.
000022- 000000 SR2: OPEN ;SWITCH REGISTER 2.
000024- 000000 SR3: OPEN ;SWITCH REGISTER 3.
000026- 140000 SR4: OPEN ;SWITCH REGISTER 4.
000030- 000274- *****
000033- 000224- ;STATUS WORD.
000034- 000224- ;MODULE START ADDR.
000036- 000001 SPOINT: MODDSP ;MODULE STACK POINTER.
000040- 000000 PASCNT: 0 ;PASS COUNTER.
000042- 000000 ICOUNT: 1 ;# OF ITERATIONS PER PASS=1
000044- 000000 SOFCNT: 0 ;LOC TO COUNT ITERATIONS
000046- 000000 HRDCHT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
000050- 000000 SDFPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS
000052- 000000 HRDPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
000054- 000000 SVSCHT: 0 ;LOC TO SAVE HARD ERRORS PER PASS
000056- 000000 RANNUM: 0 ;# OF SYS ERRORS ACCUMULATED
000058- 000000 RES1: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
000060- 000000 RES2: 0 ;RESERVED FOR MONITOR USE
000062- 000000 SVR0: OPEN ;RESERVED FOR MONITOR USE
000064- 000000 SVR1: OPEN ;LOC TO SAVE R0.
000066- 000000 SVR2: OPEN ;LOC TO SAVE R1.
000068- 000000 SVR3: OPEN ;LOC TO SAVE R2.
000070- 000000 SVR4: OPEN ;LOC TO SAVE R3.
000072- 000000 SVR5: OPEN ;LOC TO SAVE R4.
000074- 000000 SVR6: OPEN ;LOC TO SAVE R5.
000076- 000000 CSRA: OPEN ;LOC TO SAVE R6.
000078- 000000 SBADR: ;ADDR OF CURRENT CSR.
000080- 000000 ACSR: OPEN ;ADDR OF GOOD DATA, OR
000082- 000000 WASADR: OPEN ;CONTENTS OF CSR.
000084- 000000 ASADR: OPEN ;ADDR OF BAD DATA, OR
000086- 000000 ASSTAT: OPEN ;STATUS REG CONTENTS.
000088- 000000 ERRTYP: ;TYPE OF ERROR.
000090- 000000 ASB: OPEN ;EXPECTED DATA.
000092- 000000 AWAS: OPEN ;ACTUAL DATA.
000094- 001272- RSTRT: RSTRT ;RESTART ADDRESS AFTER END OF PASS
000096- 000000 WDT0: OPEN ;WORDS TO MEMORY PER ITERATION
000098- 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
000100- 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION

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000122- 000120 IDNUM: 120 ;MODULE IDENTIFICATION NUMBER=120
000224- *****
332 ;*****
333 ;*OPTIONAL USER SUPPLIED IN
334 000224- 000000 CISTRCH: -WORD 0 ;USER SUPPLIED LAST SAMPLED CH
335 000226- 000000 WLSTCH: -WORD 0 ;USER SUPPLIED LAST NOISE CH.
336 000230- 000002 OFFALL: -WORD 2 ;USER SUPPLIED TOLERANCE FOR SAMPLE
337 ;*
338 ;*REGISTER AND VECTOR ADDRESS
339 ;*
340 000232- 170400 ADNR: -WORD 170400 ;A/D CSR ADDRESS
341 000234- 000000 DAC: -WORD 0 ;DAC (WRITE ONLY) AND BUFFER REG (READ ONLY)
342 000234- 170402 ADR: -WORD 170402 ;THE FOLLOWING ARE KW1K ADDRESSES IF A KW1K EXISTS.
343 ;*
344 000236- 170420 ASR: -WORD 170420 ;CLOCK A CSR.
345 000240- 170422 ABR: -WORD 170422 ;CLOCK A PRESET BUFFER.
346 ;*
347 ;*FLAGS, COUNTERS AND OTHER REGISTERS
348 ;*
349 ;*
350 ;*
351 WHO: -WORD 0 ;0=RMS NOISE, 1=PEAK NOISE
352 INTFLG: -WORD 0 ;USED IN LOGIC TEST TO INDICATE AN A/D INTR.
353 FRED: -WORD 0 ;USED TO HOLD CURRENT DAC VALUE.
354 EDGE: -WORD 0 ;HOLD CURRENT EDGE VALUE.
355 TRP: -WORD 0 ;TEMP WORKING AREA.
356 ARMX: -WORD 0 ;USED TO HOLD RMS NOISE VALUE.
357 ARMLIN: -WORD 50. ;RMS NOISE LIMIT.
358 APKX: -WORD 0 ;USED TO HOLD A/D PEAK NOISE VALUR.
359 APKLIN: -WORD 200. ;A/D PEAK NOISE LIMIT.
360 NCH: -WORD 0 ;CURRENT NOISE CHAN.
361 CCH: -WORD 0 ;CURRENT SAMPLE CHAN.
362 PASSCNT: -WORD 0 ;PASS CNT.
363 TRNPI: -WORD 0 ;TEMPORARY STORAGE FOR ASCII CONVERSION
364 ;*
365 ;*
366 ;*
367 ;*
368 ;*
369 ;*
370 000274- 012767 020064 177616 START: MOV #B244, INTR ;B244 INTERRUPTS/ITERATION
371 000302- 012767 013000 177604 MOV #5632, WDT0 ;5632 WORDS TO MEM/ITERATION
372 000310- 012767 013000 177600 MOV #5632, WDFR ;5632 WORDS FROM MEM/ITERATION
373 000316- 016700 177464 MOV ADDR, R0 ;GET BASE ADDR OF A/D.
374 000322- 010060 000002 MOV R0, ADNR ;FIX A/D CSR ADDR.
375 000326- 062700 000002 MOV R0, ADNR ;BUFFER REG=CSR+2.
376 000332- 010067 177676 MOV #2, R0 ;FIX BUFFER REG ADDR.
377 000336- 005067 177722 CLR R0, NCH ;CLEAR 1ST CHAN. FOR NOISE.
378 ;*****
379 ;*****
380 ;*****
381 ;*****
382 000342- 104421 000000- 000256- BTUNS, BEGIN, ARMLIN, DECIM ;CONVERT ARMLIN TO ASCII AND
383 000350- 002740- ;STORE AT DECIM
384 ;*****

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385  
386  
387 000352* 116767 002364 002571  
388 000360* 116767 002357 002565  
389 000366* 116767 002352 002560  
390  
391  
392  
393  
394 000374* 104421* 000000* 000262*  
395 000402* 002740*  
396  
397  
398 000404* 116767 002332 002551  
399 000412* 116767 002325 002545  
400 000420* 116767 002320 002540  
401  
402  
403  
404  
405  
406  
407  
408  
409 000426* 005777 177600 LOG1: TST @ADSR ;ADDRESS THE A/D  
410  
411  
412  
413  
414  
415  
416 000432* 104407 000000*  
417 000432* 104407 000000* LOG2: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...  
418 000442* 032767 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.  
419 000442* 032767 000001 177346 BIT #BIT0,SRI ;IS CLOCK OPTION SELECTED?  
420 000450* 001402 BEQ LOG3 ;BR IF NOT TO NEXT TEST.  
421  
422 000452* 005777 177560 TST @ASR ;CLOCK WAS SELECTED, WILL IT RESPOND?  
423  
424  
425  
426  
427  
428 000456* 005067 177562 LOG3: CLR INTFLG ;CLEAR A/D DID INTR. FLAG.  
429 000462* 012777 000554* 177320 MOV #1,@VECTOR ;SET UP VECTOR ADDR.  
430 000470* 012777 000101 177534 MOV #101,@ADSR ;START A CONVERSION, SHOULD INTERRUPT.  
431 ; BEFORE BREAK TIME IS OVER.  
432  
433 000476* 104407 000000* BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...  
434 000502* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.  
435  
436 000506* 005767 177532 TST INTFLG ;DID THE A/D INTERRUPT?  
437 000512* 001027 BNE LOG4 ;IF SO, NEXT TEST  
438 000514* 016767 177512 MOV @ADSR,CSRA ;SET ADDR. OF AD CSR FOR ERROR TYPEOUT.  
439 000522* 017767 177504 177356 MOV @ADSR,ACSR ;RECORD CONTENTS OF CSR.  
440 000530* 005077 177476 CLR @ADSR ;STOP A/D
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441 000534* 012767 000023 177344 MOV #23,ERRTYP ;DEV FAILED TO INTERRUPT  
442  
443 000542* 104405 000000* 000000 HDRERS,BEGIN,NULL ;A/D FAILED TO INTERRUPT  
444  
445 000550* 104410 000000* ENDS,BEGIN ;  
446  
447  
448 000554* 005077 177452 1S: CLR @ADSR ;STOP A/D.  
449 000560* 005777 177450 TST @ADSR ;CLEAR CSR BIT07.  
450 000564* 005267 177454 INC INTFLG ;INDICATE THAT IT DID INTR.  
451 000570* 000002 RTI ;EXIT INTR.  
452  
453  
454  
455  
456  
457  
458  
459  
460 000572* 032767 000001 177216 LOG4: BIT #BIT0,SRI ;IS THE CLOCK OPTION SELECTED?  
461 000600* 001451 ;IF NOT, GOTO NEXT TEST.  
462  
463 000602* 012777 177777 177430 MOV #177777,@RBR ;PRESET CLOCK FOR ALL ONES.  
464 000610* 012777 000040 177414 MOV #BIT5,@ADSR ;SET OVERFLOW ENABLE IN A/D.  
465 000616* 012777 000011 177412 MOV #11,@ASR ;START CLOCK, 1MHZ, GO.  
466 000624* 005000 CLR R0 ;SET FOR DELAY LOOP.  
467  
468 000626* 104407 000000* 1S: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...  
469 000626* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.  
470 000636* 105777 177374 TSTB @ASR ;IS THE CLOCK OVERFLOW SET?  
471 000642* 100402 BNE EXIT LOOP ;YES-EXIT LOOP  
472 000644* 105200 INCB R0 ;NO-IS DELAY EXCEEDED?  
473 000646* 105200 BNE 1S ;NO-CONTINUE DELAY.  
474 000650* 001367  
475 000650* 017767 177362 2S: MOV @ASR,ASTAT ;RECORD CONTENTS OF CLOCK CSR.  
476 000650* 005077 177354 CLR @ASR ;CLEAR THE CLOCK.  
477  
478 000662* 105777 177344 TSTB @ADSR ;IS DONE FLAG SET?  
479 000668* 100416 BNE 1S ;IF YES NEXT TEST.  
480  
481 000670* 016767 177336 177202 MOV @ADSR,CSRA ;RECORD A/D CSR ADDR.  
482 000676* 017767 177330 177176 MOV @ADSR,ACSR ;RECORD CONTENTS OF A/D CSR.  
483 000704* 012767 000046 177174 ;*****ERRTYP***** CLK FAILED TO TRIGGER A/D CONVERSION  
484  
485 000712* 104405 000000* 000000 HDRERS,BEGIN,NULL ;CLOCK OVERFLOW FAILED TO TRIGGER A/D CONVERSION  
486  
487  
488  
489  
490  
491  
492 000720* 104410 000000* ENDS,BEGIN ;FOR THIS ERROR YOU MIGHT CHECK  
493 ;TO SEE IF A OVERFLOW IS WIRED TO  
494 ;A/D INPUT.  
495 ;DROP THIS MODULE - FATAL ERROR.  
496 ;CAN'T CONTINUE IF OVERFLOW DOESN'T TRIGGER THE CONVERST
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497
498
499
500
501 000724-
502 000724- 104407 000000- LOG5:
503 000730- 104407 000000-
504 000734- 005777 177274-
505 000740- 012777 001016- 177042
506 000746- 012777 000105- 177256
507 000754- 104400 000000-
508 000760- 005777 177250-
509
510
511 000764- 001417
512 000766- 016767 177240 177104
513 000774- 017767 177232 177190
514 001002- 012767 000026 177076
515
516 001010- 104405 000000- 000000
517
518
519
520 001016- 2S:
521
522 001016- 000004 000000- 000760-
523
524
525
526
527
528
529
530 001024- 012777 001104- 176756 LOG6:
531 001032- 012777 000505- 177172
532 001040- 104400 000000-
533 001044- 022777 007777 177162
534 001052- 001417
535 001054- 016767 177152 177016
536 001062- 017767 177144 177012
537 001070- 012767 000026 177010
538
539 001076- 104405 000000- 000000
540
541
542 001104- 2S:
543
544 001104- 000004 000000- 001044-
545
546
547
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552
  
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553 001112-
554 001112- 104407 000000- LOG7:
555 001116- 104407 000000-
556 001122- 005777 177106-
557 001128- 005077 177100-
558 001132- 005067 177130-
559 001136- 012777 001264- 176644
560
561 001144- 012700 177770
562 001150- 005001
563 001152- 016767 177110 177072
564 001160- 000367 177066
565 001164- 052767 000101 177060
566 001172- 016777 177054 177032
567 001200- 104400 000000-
568 001204-
569 001204- 067701 177024
570
571
572 001210- 005200
573 001212- 100767
574
575 001214- 006201
576 001216- 006201
577 001220- 006201
578 001222- 005501
579 001224- 016702 177036
580 001230- 006302
581 001232- 001662 003262-
582
583 001236- 005267 177024
584 001242- 026767 177020 176756
585 001250- 03735
586 001252- 026767 177010 176744
587 001260- 003731
588 001262- 000403
589
590
591
592
593
594 001264- 4S:
595
596 001264- 000004 000000- 001204-
597
598 001272- 005077 176734
599 001276- 005067 176766
600
601
602 001302-
603 001302- 016700 176502
604 001306- 012720 002230-
605 001312- 116710 176474
606
607
608
  
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609 001316 012700 000657 AD RMS1: MOV #431, R0 ;R0 = 84-13% OF 512 CONVERSIONS
610 001322 016767 176736 MOV NCCCH, TMP ;GET THE CHAN #
611 001330 020367 176516 SWAB TMP ;PUT IN PROPER CSR POSITION.
612 001334 052767 000100 BFC #100, TMP ;ADD INTR. ENABLE.
613 001342 016701 176716 MOV NCCCH, R1 ;PICK UP CH NUMBER.
614 001346 006301 176660 INC R1 ;FORM AN OFFSET.
615 001350 005267 176660 INC NCCSAM(1), EDCE ;GET EDGE VALUE FOR THIS CH.
616 001356 005267 176660 MOV TMP, RADSAR ;INDICATE RMS NOISE TEST.
617 001362 016777 176664 MOV PC, SAR ;CH NO. AND INTERRUPT ENABLE
618 001370 016701 176640 MOV DAC, R1 ;R1 = ADDRESS OF SAR DAC
619 001374 004767 176642 JSR PC, SAR ;GET DAC VALUE THAT PRODUCES 16/84 SPLIT
620 001400 016705 000121 MOV #81, R0 ;SAVE LEFT BOUNDARY
621 001404 012700 000502 JSR PC, SAR ;GET LEFT BOUNDARY CONVERSIONS
622 001410 004767 000502 MOV R5, ARMK ;R5 = 15-87% OF 512
623 001414 166705 176626 JSR PC, SAR ;GET DAC VALUE THAT PRODUCES 84/16 SPLIT
624 001420 010567 176630 SUB R5, ARMK ;R5 = BREADTH OF NOISE @ 68% AREA
625 001424 020567 176626 MOV R5, ARMLIM ;SAVE FOR DAC NOISE CALCULATIONS
626 001430 003413 000121 CMP R5, ARMLIM ;< OR = RMS LIMIT?
627 001432 004767 000121 JSR PC, ERRCOM ;IF WITHIN LIMIT THEN CONTINUE AT AD RMS2
628 ;GET ERROR PARAMETERS
629 001436 012767 000031 MOV #31, ERRRTYP ;ERROR PARAMETERS LOADED BY ".ERRCOM"
630 ;*****
631 001444 104405 000000 HRDERS, BEGIN, NULL ;RMS NOISE ERROR-SEE NEXT TYPEOUT
632 ;*****
633 001452 104403 000000 MSGNS$, BGIN, MSG1 ;ASCII MESSAGE CALL WITH COMMON HEADER
634 ;*****
635 ;CALCULATE A/D PEAK NOISE USING VERNIER DAC.
636
637 001460 012700 000775 AD PK1: MOV #509, R0 ;FOR PAK OF 2 1/2 SIGMA
638 001464 016767 176574 MOV NCCCH, TMP ;GET CHAN. NUMBER.
639 001470 020367 176554 SWAB TMP ;PUT IN CORRECT CSR POSITION.
640 001476 052767 000100 BFC #100, TMP ;ADD INTR. ENABLE.
641 001504 005067 176532 CLR WHO ;INDICATE RMS NOISE TEST.
642 001510 016777 176536 MOV TMP, RADSAR ;CH AND INTERRUPT ENABLE AND CH
643 001514 004767 176532 MOV DAC, R1 ;R1 = ADDRESS OF SAR DAC
644 001522 004767 000370 JSR PC, SAR ;GET DAC VALUE THAT GIVES .6% LOW COUNT
645 001526 016705 176514 MOV R3, R0 ;R3 = LEFT BOUNDARY
646 001532 012700 000003 JSR PC, SAR ;CHANGE SPLIT FOR RIGHT BOUNDARY
647 001536 004767 000003 MOV R5, ARMK ;GET DAC VALUE THAT GIVES .6% HIGH COUNT
648 001542 166705 176500 SUB R5, ARMK ;R5 = A/D PEAK TO PEAK NOISE
649 001546 010567 176506 MOV R5, APKX ;SAVE FOR DAC NOISE CALCULATIONS
650 001550 020567 176504 CMP R5, APKLIM ;< OR = A/D PEAK NOISE LIMIT?
651 001556 003413 000102 BLE ENDP ;BRANCH IF NO ERROR
652 001560 004767 001062 JSR PC, ERRCOM ;GET ERROR PARAMETERS
653 ;*****
654 001564 012767 000031 MOV #31, ERRRTYP ;ERROR PARAMETERS LOADED BY ".ERRCOM"
655 ;*****
656 001572 104405 000000 HRDERS, BEGIN, NULL ;A/D NOISE LIMIT EXCEEDED
657 ;*****
658 001600 104403 000000 MSGNS$, BGIN, MSG5 ;PEAK NOISE ERROR-SEE NEXT TYPE OUT
659 ;*****
660 ;ASCII MESSAGE CALL WITH COMMON HEADER
661
662 001606 005267 176456 ENDP: INC PASSCNT ;UPDATE PASS COUNT.
663 001610 032767 000004 BIT #BIT2, SR1 ;DOING MULTIPLE NOISE CHANNELS?
664 001622 026727 176442 BNE 1S ;YES - GET THE NEXT ONE
665 001622 026727 176442 CMP PASSCNT, #13 ;DONE ENOUGH PASSES?

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665 001630 001417 BFC #3S ;YES-DO ENDPASS.
666 001632 032767 BIT #BIT0, SR1 ;ARE WE CONNECTED TO THE KW11K OPTION?
667 001640 001013 BNE 3S ;IF SO THE 1 MIN IS UP ON ONE PASS.
668
669 001642 000167 JMP AD RMS1 ;NO DO AGAIN.
670
671 001646 005267 INC NCCCH, NLSTCH ;POINT TO NEXT CH.
672 001652 026767 176412 CMP NCCCH, NLSTCH ;EXCEED LAST NOISE CH?
673 001660 001760 176346 BLOS BLOS ;NO-TEST THIS CH
674 001662 005067 CLR NCCCH ;YES - START AGAIN ON CH. 0.
675 001666 000755 BR 1S ;GO TEST CH 0.
676
677 001670 032767 BIT #BIT1, SR1 ;ARE WE DOING VOLTAGE SAMPLING?
678 001678 001505 BFC #CCH ;NO - END PASS1
679 001700 005067 CLR CCH ;YES - START WITH CH 0.
680
681 001704 026767 CMP CCH, CLSTCH ;DONE ALL CHANNELS?
682 001712 003077 BGT ENDP ;YES - REPORT END PASS.
683 001714 005003 CLR R3 ;R3 WILL HOLD SAMPLE.
684 001716 012700 MOV #83, R0 ;SET TO TAKE 8 SAMPLES
685 001722 012777 001752 MOV #63, RVECTOR ;SET VECTOR FOR INTERRUPT
686 001730 016701 176332 MOV CCH, R1 ;GET CH. NUMBER
687 001734 000301 SWAB R1 ;FIX RIGHT POSITION IN CSR.
688 001736 052701 #101, R1 ;ADD INTR. ENABLE AND GO.
689 001742 010177 176264 MOV R1, RADSAR ;START A/D.
690 001746 104400 000000 EXITS, BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
691 001752 000004 000000 PTRQS$, BEGIN, 7S ;*****
692 ;*****
693 001752 000004 000000 PTRQS$, BEGIN, 7S ;*****
694 ;*****
695 ;*****
696 001760 067703 176250 7S: ADD @ADR, R3 ;ADD THIS SAMPLE TO TOTAL
697 001764 005300 DEC R3 ;ALL DONE ALL SAMPLES?
698 001766 001365 BNE 5S ;NO - DO NEXT ONE.
699
700 001770 006203 ASR R3 ;YES NOW WE
701 001772 006203 ASR R3 ;MUST DIVIDE BY
702 001774 006203 ASR R3 ;8 TO GET THE
703 001776 005503 ADC R3 ;SAMPLE AVERAGE.
704 002000 016700 176262 MOV CCH, R0 ;NOW GET CH NUMBER
705 002004 006300 ASL R0 ;FORM AN OFFSET
706 002006 010301 MOV R3, R1 ;SAVE NEW SAMPLE VALUE.
707 002010 166003 SUB RECSAM(0), R3 ;GET THE DIFFERENCE BETWEEN
708 ; AND THE NEW ONE WE JUST GOT
709 002014 100001 BPL 8S ;IF POSITIVE WE'RE OK.
710 002016 005403 NEG R3 ;OTHERWISE MAKE IT POSITIVE.
711 002018 020367 CMP R3, OFFALL ;IS IT WITHIN TOLERANCE?
712 002024 003426 BLE 1S ;YES DO NEXT CH
713 ;*****
714 ;*****
715 ;*****
716 ;*****
717 002026 104420 000000 000266 OTOAS$, BGIN, CCH, CHANN ;CONVERT CCH TO ASCII AND
718 002034 003502 ;STORE AT CHANN
719 ;*****
720 ;*****

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721 002036 016067 003262 176226      MOV     RECSAM(0),TEMP1 ;GET OLD VALUF
722 ;*****
723 ;CONVERT TEMP1 TO ASCII AND
724 ;STORE AT ULDS
725 002044 104420 000000 000272  OTOAS,BEGIN,TEMP1,ULDS
726 002052 003462
727 ;*****
728
729 002054 010167 176212      MOV     R1,TEMP1 ;GET NEW VALUF
730 ;*****
731 ;CONVERT TEMP1 TO ASCII AND
732 ;STORE AT NEWS
733 002060 104420 000000 000272  OTOAS,BEGIN,TEMP1,NEWS
734 002066 003472
735 ;*****
736
737 002070 010160 003262  MOV     R1,RECSAM(R0) ;PUT NEW INTO OLD.
738
739 002074 104403 000000 003024  MSGNS,BEGIN,MSG12 ;ASCII MESSAGE CALL WITH COMMON HEADER
740
741 002102 005267 176160 10S: INC     CCH ;LOOK AT NEXT CHAN.
742 002106 000167 176572      JMP     4S ;GO TEST IT
743
744 002112  ERNDP:  ENDP: ;SIGNAL END OF ITERATION.
745 002112 104413 000000  ;MONITOR SHALL TEST END OF PASS
746
747 ;USING SUCCESSIVE APPROXIMATION AND VERNIER DAC DEFINED IN R1.
748
749 SAR:  MOV     #200,R2 ;R2 = MSB OF DAC
750 CLR     (R1) ;GET RID OF "ONES"
751 CLR     FRED ;START WITH ZERO DAC
752 BIT:  ADD     R2,FRED ;TRY THIS BIT
753 MOV     FRED,(R1) ;LOAD DAC
754 CLR     R3 ;INIT HIGH COUNT
755 MOV     #512,R4 ;R4 = # OF SAMPLES IN A BURST
756 CONW:
757
758 002146 032767 000001 175642  BIT     #BIT0,SRI ;IS CLOCK SELECTED?
759 002154 001004      BNE     1S ;YES GOTO HANDLER
760
761 002156 052777 000001 176046  BIS     #BIT0,@ADSR ;NO - SET GO BIT IN A/D.
762 002164 000420      BR      2S ;GOTO 2S
763
764 002166 004767 000364 1S:  JSR     PC,RANDY ;GET A RANDOM NUMBER.
765 002172 005077 176040 ;MAKE SURE THE CLOCK'S CSR IS CLEAR.
766 002176 052767 177770 000434 ;MAKE SURE OF HIGH NUMBER.
767 002180 016777 000430 176026 ;SET CLOCK PRESET REG.
768 002182 052777 000040 176012 ;SET OVERFLOW ENABLE.
769 002184 012777 000011 176010 ;START CLOCK
770 002226 104400 2S:  MOV     #11,@ASR ;RETURN TO MONITOR.
771
772 WAIT:  EXITS
773
774 002230 000004 000000 002236  PIR0S,BEGIN,1S
775 ;-----
776 ;

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777 002236 027767 175772 176004 1S:  CMP     @ADDR,EDGE ;> OR = EDGE?
778 002244 103401      BLO     2S ;BRANCH IF <EDGE
779 002246 005203      INC     R3 ;COUNT IF > OR =EDGE
780 002250 001335      DFC     R4 ;COUNT THROUGH BURST
781 002252 001335      CONW   R4 ;BRANCH IF NOT DONE
782 002254 020300      CMP     R3,R0 ;HIGH COUNT > 1/2 OF 512 CONVERSIONS?
783 002256 003402      BNE     3S ;BRANCH TO LAVE BIT IN
784 002260 003402      SUB     R2,FRED ;TAKE BIT OUT
785 002264 002507      AND    R2,R2 ;NEXT BIT
786 002266 001320      BNE     BIT ;NEXT BIT
787 002270 005767 175752      BIT     1S ;CHECK FOR ALL "ZFROES"
788 002274 001405      BEQ     WRPERR ;CHECK FOR ALL "ONES"
789 002276 026727 175744 000377 ;BRANCH IF INVALID RESULT
790 002304 001401      CMP     FRED,#377 ;CHECK FOR ALL "ONES"
791 002306 000207      BEQ     WRPERR ;BRANCH IF INVALID RESULT
792 ;RETURN TO ADMST OR ADPK1.
793 ;VOLTAGE NEEDED TO PRODUCE # OF HIGH COUNTS SPECIFIED IS OUT OF THE
794 ;RANGE OF THE WRAPAROUND DAC.
795 ;CLEAR INTERRUPTS, PRINT MESSAGE AND DROP MODULE.
796
797 002310 005077 175716  WRPERR: CLR     @ADSR ;STOP A/D
798 002314 004767 000326      JSR     PC,ERCOM ;GET ERROR PARAMETERS
799 ;ERROR PARAMETERS LOADED BY "FRCOM"
800 002320 012767 000031 175560  MOV     #11,ERRTYP ;A/D NOISE LIMIT EXCEEDED
801 002326 104405 000000 000000 ;*****
802 ;NOISE TEST WRAPAROUND ERROR
803 002334 005767 175702 ;*****
804 002340 001004      TST     WHO ;SER WHICH TEST WE WERE DOING.
805 002342 104403 000000 003006  BNE     1S ;WHO=1 THEN RMS NOISE TEST.
806 002350 000403      MSGNS,BEGIN,MSG11 ;ASCII MESSAGE CALL WITH COMMON HEADER
807 002352 104403 000000 003046  BR      2S ;GO AHEAD.
808 002360 005267 175460 1S:  MSGNS,BEGIN,MSG13 ;ASCII MESSAGE CALL WITH COMMON HEADER
809 002364 005726      INC     HRDCNT ;UPDATE THE ERROR COUNT.
810 002366 000167 177214      TST     (6)+ ;RESTORE STACK FROM THE JSR PC THAT
811 ;SET US TO "SAR".
812 ;GET NEXT DIRFCTIVE.
813
814 ;GET THE AVERAGE OF 512 SAMPLES AND THEIR COUNT SPREAD
815
816 AVER:  CLR     R0 ;INIT R0 FOR RUNNING SUM
817 002372 005000      MOV     #512,R4 ;BURST OF 512 CONVERSIONS
818 002374 012704 001000      MOV     #BIT15,R1 ;INIT HIGH COUNT TO NEGATIVE #
819 002400 012701 100000      MOV     #BIT14,R2 ;INIT LOW COUNT TO POSITIVE #
820 002404 012702 040000
821
822 ECON:
823 002410 012702
824 002410 032767 000001 175400  BIT     #BIT0,SRI ;IS CLOCK SELECTED?
825 002416 001004      BNE     1S ;YES GOTO HANDLER
826
827 002420 052777 000001 175604  BIS     #BIT0,@ADSR ;NO - SET GO BIT IN A/D.
828 002426 000420      BR      2S ;GOTO 2S
829
830 002430 004767 000122 1S:  JSR     PC,RANDY ;GET A RANDOM NUMBER.
831 002434 005077 175576 ;MAKE SURE THE CLOCK'S CSR IS CLEAR.
832 002440 052767 177770 000172 ;MAKE SURE OF HIGH NUMBER.
833 002446 016777 000166 175564 ;SET CLOCK PRESET REG.

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833 002454 052777 000040 175250 BIS #R15,@ASR ;SET OVERFLOW ENABLF.
834 002462 012777 000011 175250 MOV #11,@ASR ;START CLOCK
835 002470 104400 000000 2S:
836 002474 104400 000000 ERINT: EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
838
839 002474 000004 000000 002502 PIRQS,BEGIN,1S -----
840 002502 067700 175526 1S: ADD @ADBR,R0 ;RO = RUNNING SUM OF DIFFERENCES
841 002506 160300 175526 SUB @ADBR,R1 ;OFFSET BY EXPECTED VALUE
842 002510 027701 175520 BLE @ADBR,R1 ;HIGHER THAN PREVIOUS HIGH?
843 002514 003402 175512 MOV @ADBR,R1 ;BRANCH IF NOT A NEW HIGH
844 002518 017702 175506 2S: MOV @ADBR,R1 ;NEW HIGH RESULT
845 002522 027702 175506 MOV @ADBR,R2 ;LOWER THAN PREVIOUS LOW?
846 002526 002002 175500 3S: BGE @ADBR,R2 ;BRANCH IF NO
847 002530 017702 175500 DEC R4 ;COUNT THROUGH BURST
848 002534 005304 175500 BNE @ADBR,R2 ;DO ANOTHER SAMPLE IF NOT DONE
849 002538 000300 175500 SWAB R0 ;DIVIDE SUM OF DIFFERENCES BY 256
850 002542 110000 175500 MOV R0,R0 ;SIGN EXTEND
851 002546 005500 175500 ASR R0 ;DIVIDE BY 2 MORE
852 002550 000300 175500 ADC R0 ;ROUND UP FOR > 1/2
853 002554 160201 175500 ADD R2,R1 ;OFFSET AVERAGE BY EXPECTED RESULT
854 002558 000207 175500 RTS PC ;R1 = COUNT SPREAD
855
856 ;GENERATE A RANDOM NUMBER
857
858
859
860 002556 066767 000060 000054 RANDY: ADD RNB,RNA ;THESE FOLLOW SEQUENCE OF
861 002560 066767 000054 000046 ADD RNC,RNA ; INSTRUCTIONS GENERATE
862 002564 005567 000042 000036 ADD RNB,RNB ; A RANDOM NUMBER
863 002568 066767 000036 000030 ADD RNC,RNC ; TO BE USED
864 002572 005567 000030 000020 ADD RNB,RNB ; TO BE LOADED
865 002576 066767 000020 000012 ADC RNB ; INTO THE CLOCK'S PRESET
866 002580 005567 000012 000006 ADD RNC,RNC ; DIFFER, IF SELECTED FOR TEST.
867 002584 066767 000006 000000 ADC RNC ; ONLY RANA WILL BE USED.
868 002588 000300 000000 RTS PC ;RETURN TO "CONV"
869 002592 142315 000000 RNB: 142315 ;RANDOM TO NUMBER A.
870 002596 127623 000000 RNC: 127623 ;RANDOM NUMBER B.
871
872 ;CONVERT NOISE RESULT TO DECIMAL
873
874
875
876
877 002646 016767 175360 175224 ERCOM: MOV @ASR,CSRA ;LOAD HEADER FOR ERROR CALL
878 002654 016767 175352 175220 MOV @ASR,ACSR
879 002662 016767 175344 175214 MOV #5,TEMP1
880 002670 010567 175336 ;*****
881 ;*****
882 ;*****
883 ;*****
884 ;*****
885 002674 104421 000000 000272 BTODS,BEGIN,TEMP1,DECIM
886 002702 002740 ;*****
887 002704 116767 000032 000323 MOV R DECIM+2,VALUE ;MOVE CONVERTED VALUES TO ASCII BUFFER
888 002712 116767 000025 000317 MOV R DECIM+3,VALUE+2 ;FOR TIMEOUT OF ERROR

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889 002720 116767 000020 000312 MOV R DECIM+4,VALUE+3 ;ON RETURN
890 ;*****
891 ;*****
892 ;*****
893 002726 104420 000000 000264 OTOAS,BEGIN,NCCCH,CHANN ;CONVERT NCCCH TO ASCII AND
894 002734 003502 ;STORE AT CHANN
895
896 002736 000207 RTS PC ;EXIT TO CALLER.
897 ;*****
898 002740 000003 DECTH: .RLKW 3
899 ;*****
900 ;ASCII MESSAGES AND POINTERS
901
902 002746 003072 MSG1: P2 ;ON CHAN
903 002750 003506 CHANN+4 ; (CHAN NUMBER 2 DIGITS)
904 002752 003064 P1 ; & A/D
905 002754 003123 P4 ; RMS
906 002756 003133 P6 ; NOISE =
907 002760 003235 VALUE ; (CONVERTED BELOW) X.XX LSR (LIMIT =
908 002762 003157 P7 ; 0.50 LSR) *THIS VALUE WILL CHANGE IF OPERATOR CHANGES
909 002764 177777 -1 ; MESSAGE TERMINATOR.
910
911 002766 003072 MSG5: P2 ;ON CHAN
912 002770 003506 CHANN+4 ; (CHAN NUMBER 2 DIGITS)
913 002772 003064 P1 ; & A/D
914 002774 003133 P5 ; PEAK
915 002776 003133 P6 ; NOISE =
916 002780 003235 VALUE ; (CONVERTED VALUE) X.XX LSR (LIMIT =
917 002782 003157 P8 ; 2.00 LSR) *THIS VALUE WILL CHANGE IF OPERATOR CHANGES
918 002784 177777 -1 ; MESSAGE TERMINATOR
919
920 003006 003064 MSG11: P1 ; & A/D
921 003010 003133 P5 ; PEAK
922 003012 003213 P3 ; WRAPAROUND
923 003014 003227 P5 ; ERROR
924 003016 003072 P2 ; ON CHAN
925 003018 003072 CHANN+4 ; (CHAN NUMBER 2 DIGITS)
926 003020 177777 -1 ; MESSAGE TERMINATOR
927
928 003024 003064 MSG12: P1 ; & A/D
929 003026 003133 P5 ; ERROR
930 003028 003072 P2 ; ON CHAN
931 003030 003506 CHANN+4 ; (CHAN NUMBER 2 DIGITS)
932 003032 003104 P3 ; OLD VALUE =
933 003034 003495 P9 ; A/D READING 4 DIGITS
934 003036 003495 NEWS+2 ; NEW A/D READING 4 DIGITS
935 003038 003474 -1 ; MESSAGE TERMINATOR.
936 003040 177777
937
938 003046 003064 MSG13: P1 ; & A/D
939 003050 003123 P4 ; RMS
940 003052 003213 P13 ; WRAPAROUND
941 003054 003072 P5 ; ERROR
942 003056 003072 P2 ; ON CHAN
943 003058 003506 CHANN+4 ; (CHAN NUMBER 2 DIGITS)
944 003060 177777 -1 ; MESSAGE TERMINATOR.

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TNP	000252R	358#	563*	564*	565*	566	610*	611*	612*	617	638*	639*	640*	642
TRPDFD=	000022	332#												
VALUE	003235R	887*	888*	889*	907		916	973#						
VECTOR	000010R	286#	429*	505*	530*	559*	603	685*						
WAIT	002230R	604	773#											
WASADR	000104R	320#												
WDFR	000116R	327#	372*											
WDT0	000114R	326#	371*											
WHO	000242R	354#	516*		641*	803								
WRPERR	002310R	788	790	796#										
XPLAC	000005R	284#												
.	= 003512R	898#	979#	980#	982#	984#	986#							

. ABS. 000000 000
 003512 001

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0

XADCBO,XADCBO/SOL/CRP:SYM=DDXCOM,XADCBO
 RUN-TIME: 22.3 SECONDS
 RUN-TIME RATIO: 29/5=5.8
 CORE USED: 7K (13 PAGES)